



Centre of Excellence in VLSI

# CERTIFICATE OF COMPLETION

*This is to certify that*

**Anurag Kumar Dubey**

*has successfully completed*

**VLSI SoC Design using Verilog HDL**

*Online Course on 12th day of January 2022*

*Celebrating*  
**10 YEARS**  
*Of Excellence*



**SIVAKUMAR P R**  
CEO, MAVEN SILICON